

New Grads recruiting for physical design engineer

QUALIFICATION (DETAIL):

Education:

BSEE or MSEE

Experience:

1. Knowledge of any part of the process from netlist handoff to tapeout is a MUST:
 - Floorplanning
 - Power planning and signoff
 - Placement, CTS and routing
 - SI effect analysis and fixing
 - Physical verification
2. Hands-on experience on above items is preferred.
3. Scripts development in Perl and TCL

DESCRIPTION OF FUNCTION & RESPONSIBILITY:

1. Implement APR from netlist to gds to close timing and routing
2. Build high efficient PG mesh to meet IR-drop and EM requirements
3. Fix SI effect
4. Perform ECO and metal spin
5. Support die size estimation
6. Develop utilities/scripts to improve design flow

Physical design engineer

QUALIFICATION (DETAIL):

Education:

BSEE with 2+ years of related experience, MSEE with 1+ years of related experience

Experience:

1. Hands-on experience of any part of the process from netlist handoff to tapeout:
 - Floorplanning
 - Power planning and signoff
 - Placement, CTS and routing
 - SI effect analysis and fixing
2. Physical verification is a MUST
3. Scripts development in Perl and TCL

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