

**QUALIFICATION (DETAIL):**

## Education:

- \_ .. BS in Electrical/Electronics Engineering, MS preferred.

## Experience:

- \_ .. Good communication skills in English.
- \_ .. Experience in Bluetooth chip design a plus
- \_ .. Must be proficient in RTL coding, logic synthesis, gate-level simulations.
- \_ .. Good knowledge of IC design backend flows.
- \_ .. Experiences in IC life-cycle from conception, design, verification, top-level netlist with pads to tape-out, chip-testing and mass-production.
- \_ .. FPGA, PCB or embedded SW skill is a plus.

**DESCRIPTION OF FUNCTION & RESPONSIBILITY:**

The Digital Design Engineer will be responsible for designing our wireless and SOC ASIC's. You will work closely with our architecture/algorithm engineers to explore ideas for next generation products and then develop RTL to turn these ideas into customer solutions.

- \_ .. Chip features specification and RTL design
- \_ .. Synthesis, verification, timing.
- \_ .. FPGA emulation, lab validation and debugging